System z Channel Architecture Operation and Problem Determination

Dauer: 5 Tage    Kurscode: OZ98D1DE

Kursbeschreibung:

This course provides an overview of current IBM System z family of servers. It gives detailed information on the System z channel architecture, System z High Performance FICON (zHPF), Fiber connection (FICON) and Enterprise Systems Connection (ESCON) channels, lists operational and protocol characteristics and use of available tools for problem determination (PD) purposes. It discusses operator commands, directors, and Hardware Management Console (HMC)/Support Element (SE) usage for channel problem determination and resolution. Also discussed is the I/O Operations component of System Automation (SA) for z/OS (SA z/OS). Its purpose, and how it is used to manage and assist in PD for large enterprises with FICON and ESCON configurations. Hands on lab exercises are included to reinforce lecture topics.

Zielgruppe:

The class should consist of hardware planners, technical support personnel, and system programmers or anyone that needs a complete understanding of the System z channel architecture and how it applies to ESCON and FICON I/O configurations.

Kursziele:

- List the System z servers (System z10, z9, and zSeries) capable of running in z/Architecture mode and the previous ESA/390 architecture
- Describe system z frame layout and cage usage
- Define PU assignments and usage
- Identify the various server components and how they are involved in a channel operation
- Processors, memory, memory busses, and channels
- Describe the System z channel connectivity provided at the CEC cage:
  - STI connectivity to I/O cage
  - System z10 HCA connectivity to I/O cage
- System z server connectivity for various types of CF links
- Define the purpose of high performance FICON (zHPF) on System z10 servers
- Describe the command structure of a channel program using ESCON, FICON or FICON zHPF channels
- Describe the various phases and control blocks used in an I/O operation (ESCON, FICON and zHPF)
- List server initialization activities and Basic/LPAR mode of operation
- Identify the different types of CHPIDs
- Describe HCD concepts needed to define and modify the I/O configuration
- Identify the components that make up the I/O device path

Voraussetzungen:

You should have an understanding of:

- Basic data processing concepts
- I/O concepts

Schulungsinhalt:

Please refer to objectives for description information.
Weitere Informationen:

Für weitere Informationen oder Buchung kontaktieren Sie uns bitte unter 0800 / 295 26 33
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