Hardware Configuration and Definition (HCD) for z/OS

Duration: 4 Days   Course Code: ES96G

Overview:

This course is designed to teach you how to use the Hardware Configuration Definition (HCD) of z/OS to create an I/O configuration and dynamically alter the I/O configuration.

Target Audience:

This course is for people responsible for maintaining the I/O configuration contained in the input/output data files (IODFs) and input/output configuration data sets (IOCDs) at their z/OS installation.

Objectives:

- Describe new zSeries processor technology

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- Code new zSeries processors (z990 to z10)

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- Code ESCON channels and ESCON CTCs

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- Code FICON channels and FICON CTCs

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- Code Coupling Facilities (CF) and CF links

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- Code cascaded FICON Director

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- Create an IODF work file on a z processor from scratch

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- Use CHPID mapping tool to create a validated work IODF

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- Use work IODF and create a production IODF

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- Perform Dynamic I/O changes on a real z/OS system

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- Build a LOADxx parmlib member for initial program load (IPL)

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- View configuration graphically

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- Create appropriate configuration reports

Prerequisites:

You should have:

- basic knowledge of z/OS and I/O configuration. This knowledge can be developed on the job or by taking Fundamental System Skills in z/OS (ES10GB).

Content:

Day 1

- (00:30) Welcome

- (01:00) Unit 1: HCD introduction
- (00:30) Unit 2: IOCP and MVSCP macro review
- (00:30) Unit 3: HCD dialog
- (01:00) Unit 4: LPAR and logical control units
- (02:00) Unit 5: ESCON Directors
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- (00:30) Unit 7: Review of zSeries hardware

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- (00:30) Exercise 1: Overview of lab environment
- (00:45) Exercise 2: HCD familiarity
- (01:30) Unit 8: z990, z9, z10, and HCD
- (01:00) Unit 9: FICON, FICON CTCs, and FICON directors
- (03:00) Exercise 3: Coding a zSeries 2094
- (06:00) Exercise 4: Add ESCON directors to your configuration
- (00:30) Exercise 5: Add FICON directors to your configuration (optional)
- (01:00) Unit 11: IPL and LOADxx member
- (00:45) Unit 12: Dynamic I/O

- (00:45) Unit 15: HCD and parallel sysplex
- (00:45) Exercise 9: Coding a 2097 using the CMT
- (00:30) Exercise 10: Coding CFs and CF links including z10 CIB links
- (00:20) Exercise 11: Coding sysplex CTCs (optional)

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- (00:30) Exercise 7: Building a LOADxx member
- (01:00) Exercise 8: Perform dynamic I/O
- (01:00) Unit 14: ESCON CTCs for sysplex
- (00:45) Unit 15: HCD and parallel sysplex
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Further Information:

For More information, or to book your course, please call us on 00 20 (0) 2 2269 1982 or 16142

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